



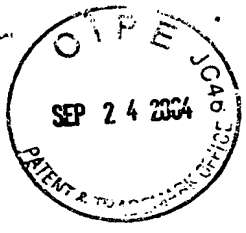
## Replacement Sheet

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	3 1		1 6	1 5		8	7		0
EAX			AH (100+!P)					AL (000)	
ECX			CH (101+!P)					CL (001)	
EDX			DH (110+!P)					DL (010)	
EBX			BH (111+!P)					BL (011)	
ESP								SPL (100+P)	
EBP								BPL (101+P)	
ESI								SIL (110+P)	
EDI								DIL (111+P)	

Note: "+P" = Instruction Includes Register Address Prefix Byte  
"+!P" = Instruction Excludes Register Address Prefix Byte

*Fig. 2*



Replacement Sheet

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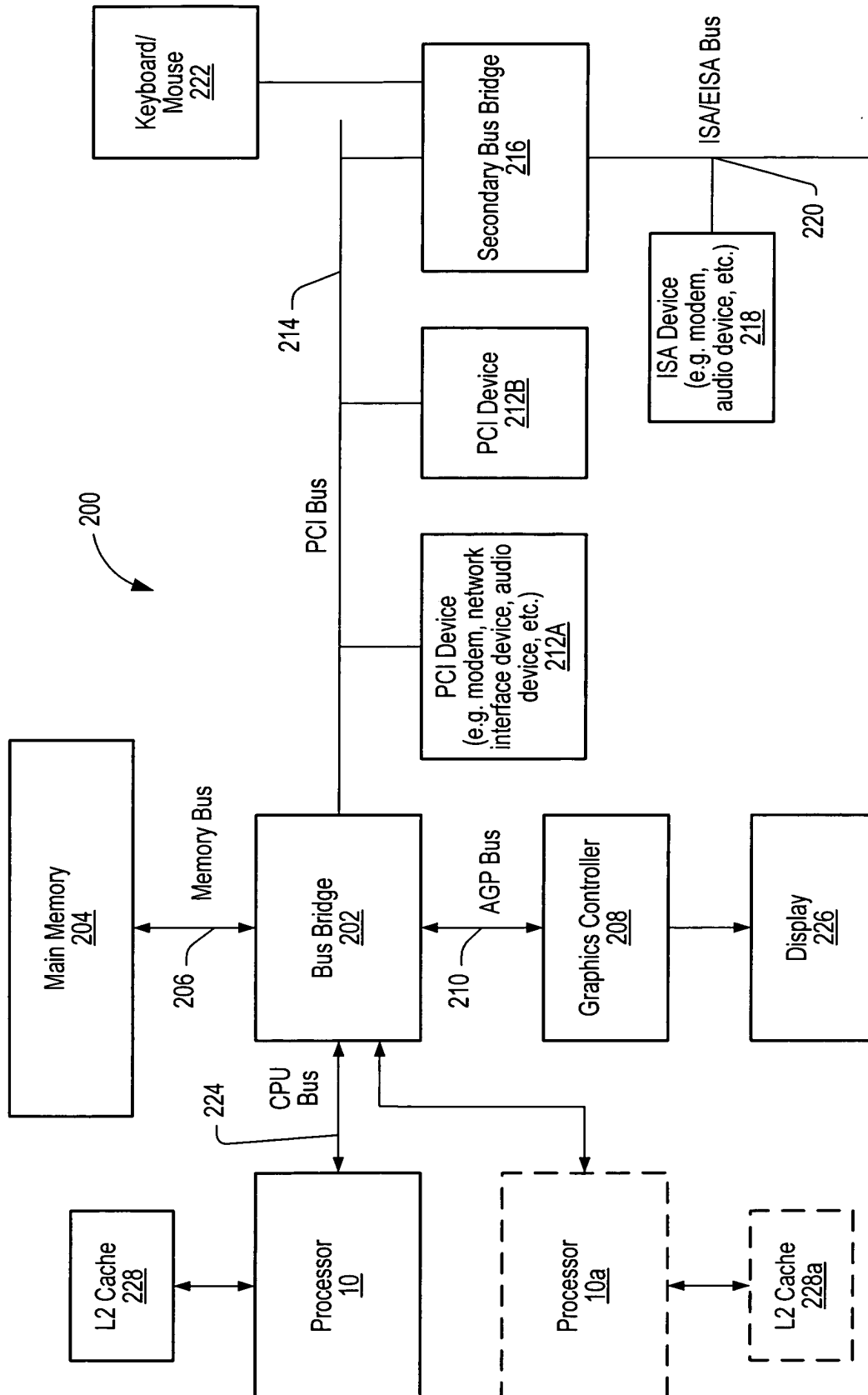
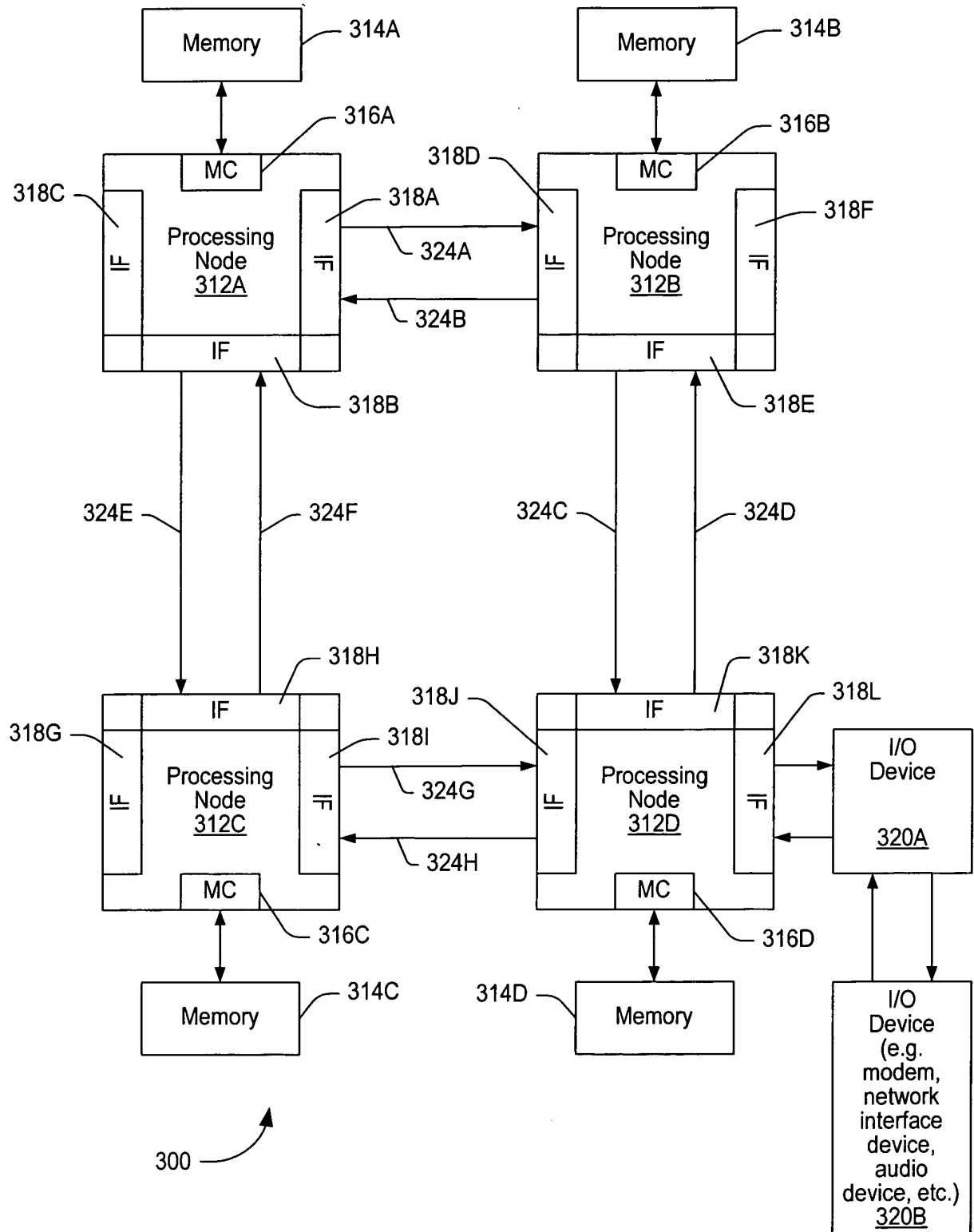


Fig. 13



*Fig. 14*